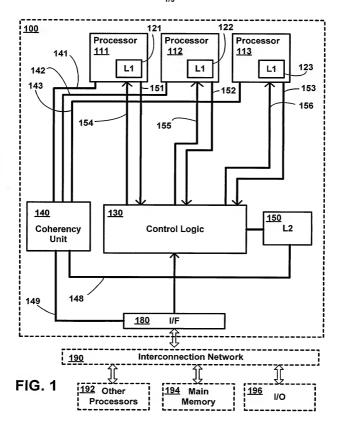
42390P12483 TRANSFER OF CACHE LINES ON-CHIP BETWEEN PROCESSING CORES IN A MULTI-CORE SYSTEM Sujat Jamil, et al.



42390P12483 TRANSFER OF CACHE LINES ON-CHIP BETWEEN PROCESSING CORES IN A MULTI-CORE SYSTEM Sujat Jamil, et al.

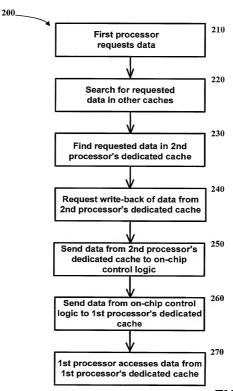
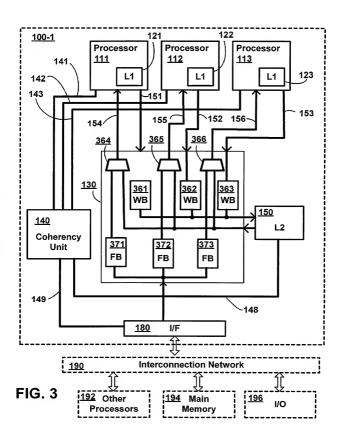


FIG. 2



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42390P12483 TRANSFER OF CACHE LINES ON-CHIP BETWEEN PROCESSING CORES IN A MULTI-CORE SYSTEM

Sujat Jamil, et al.

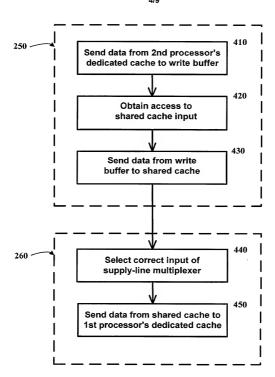
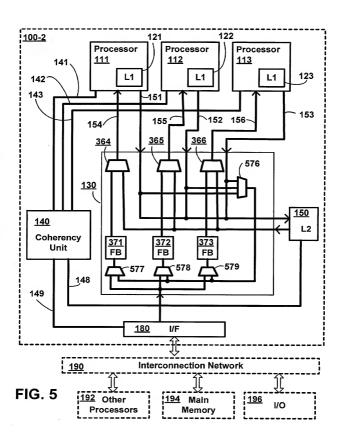


FIG. 4



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42390P12483 TRANSFER OF CACHE LINES ON-CHIP BETWEEN PROCESSING CORES IN A MULTI-CORE SYSTEM Sujat Jamil, et al.

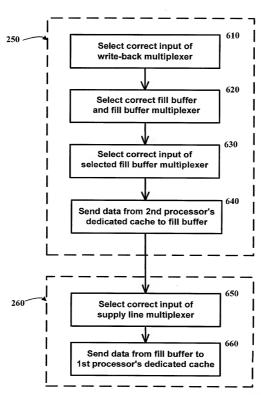
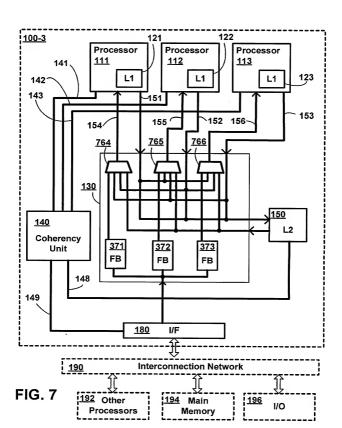


FIG. 6



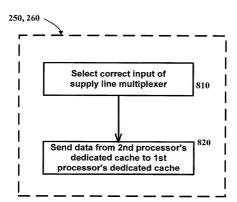


FIG. 8

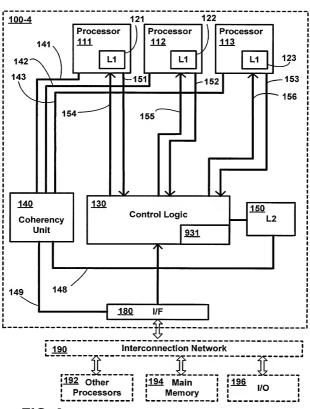


FIG. 9